

#5/EDS Supp
D. Duford
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/882,534
Filing Date June 14, 2001
Inventor Garo J. Derderian, et al
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Unknown
Attorney's Docket No. MI22-1752
Title: Capacitor Fabrication Methods and Capacitor Constructions

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

Assistant Commissioner for Patents
Washington, D.C. 20231

REMARKS

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is divisional application of co-pending application Serial No. 09/652,532, filed August 31, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Date: 11 Jan 2002

By: James E. Lake
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